Terms to learn   
  
All about Page!! & Page replacement   
Memory traces  
Memory event   
Page table   
  
4-memory traces (one million trace memory)

* Bzip
* Gcc
* Sixpacks
* Swim

Unzip command for the above set of traces  
> gunzip –d gcc.trace.gz

We have test traces for testing our page replacement algorithm   
1) Rand (22.4 page 6)  
2) Lru ( )  
3) clock (22.8 Approximating LRU)

What to build   
  
A simulator that reads a memory trace and simulates the action of virtual memory system with single level page table.  
  
Need to keep track of what pages are loaded into the memory and also the simulator excepts 4 argument

1. Name of the memory trace file (I.e Bzip,Gcc,sixpacks,swim)
2. The number of page frame in simulated memory : not sure what it is
3. Page replacement algorithm to use : rand/lru/clock
4. The mode to run : quiet/debug : no sure what it is

In Debug mode simulator prints out the details of each event   
In quiet mode the simulator: Prints out summary of disk accesses and page fault rate.  
  
Since it is just a Simulation of page table : we not need to write or read data from disk   
we just have to keep track of the read and write with a counter.

Skeleton of the code has   
Most of the input (reading a trace), simulation counters and output messages has already being implemented in the skeleton files provided for you.

In Python   
  
memsim.py : main file : contains reference to clockmmu.py,lru.py,randmmu.py and mmu.py